

FIG. 1 (CR-ECS configuration)

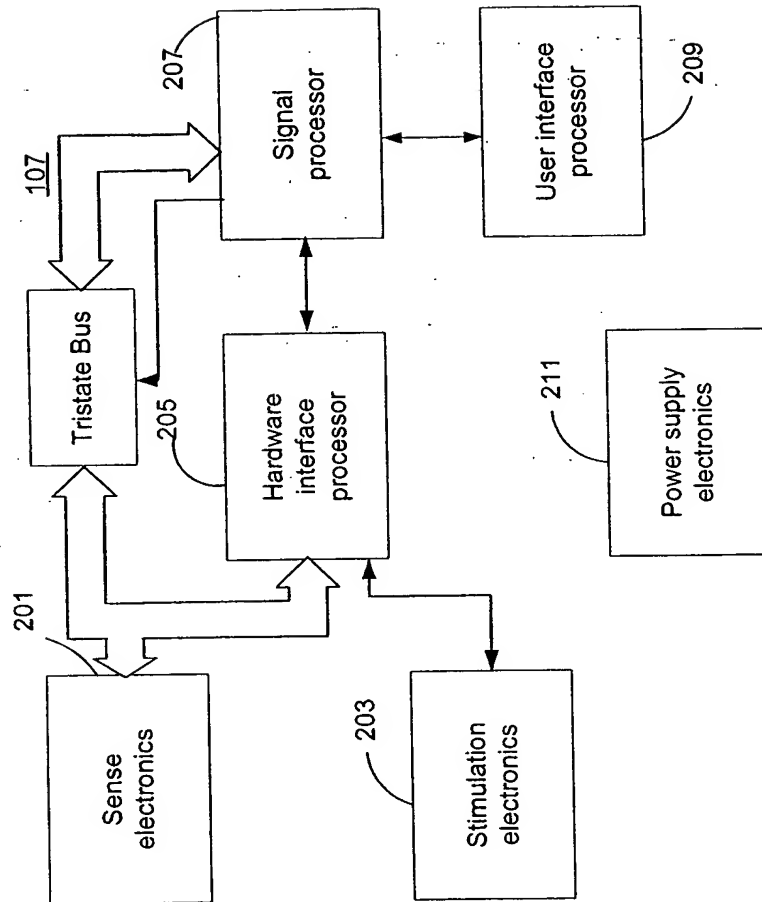


FIG. 2 (Bedside device configuration)

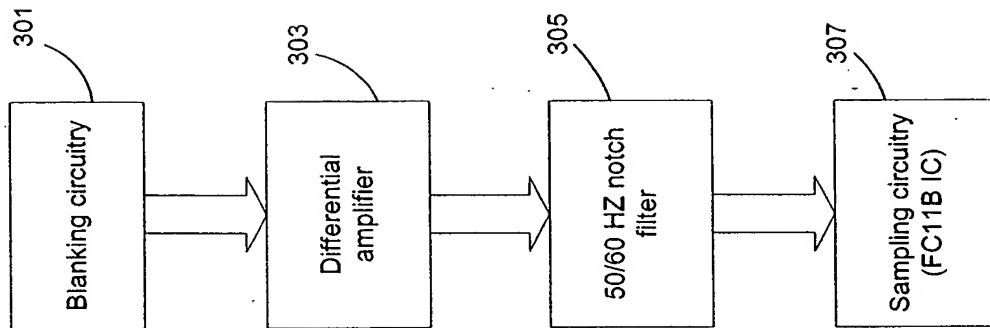


FIG. 3
Sense electronics configuration

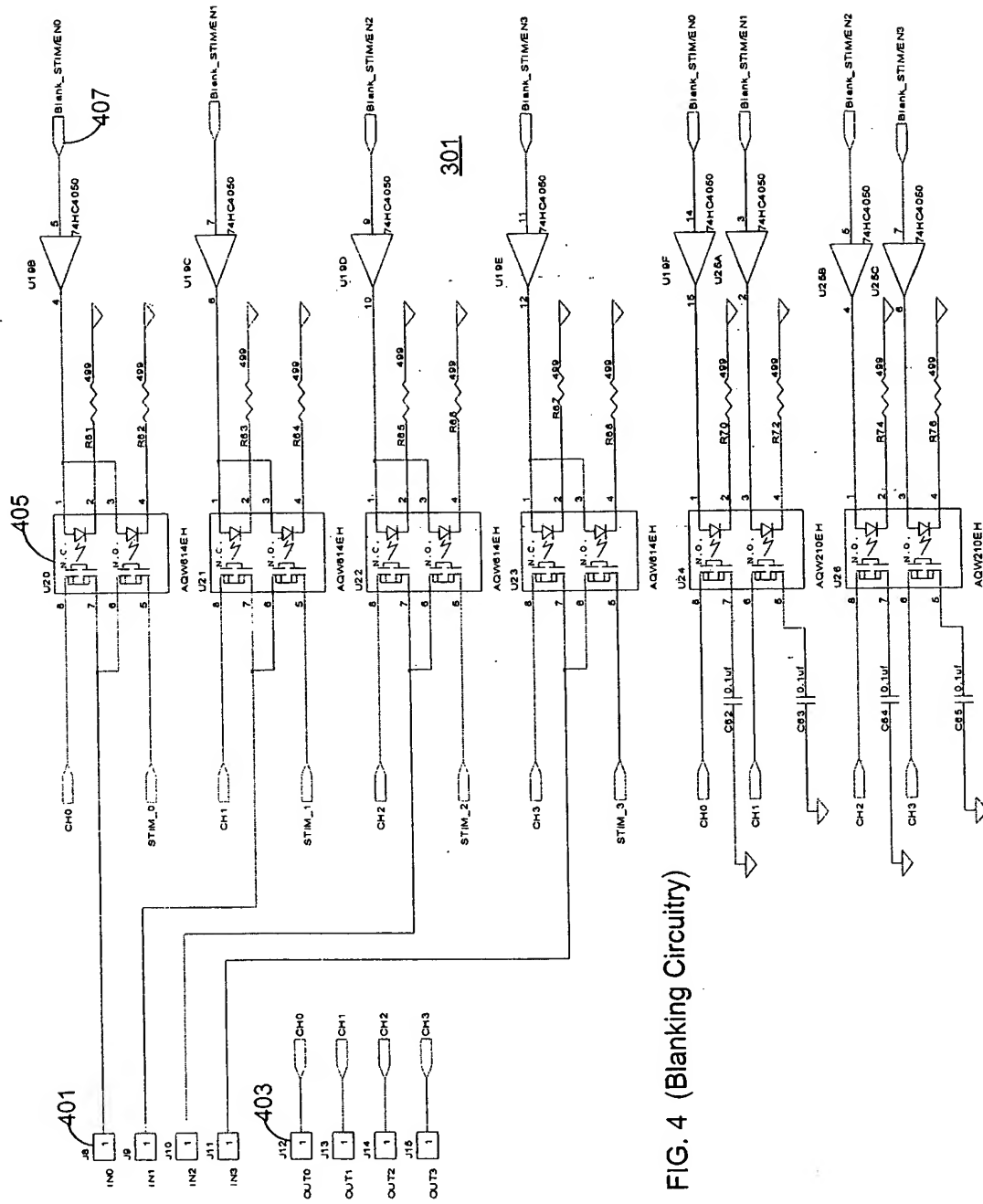


FIG. 4 (Blanking Circuitry)

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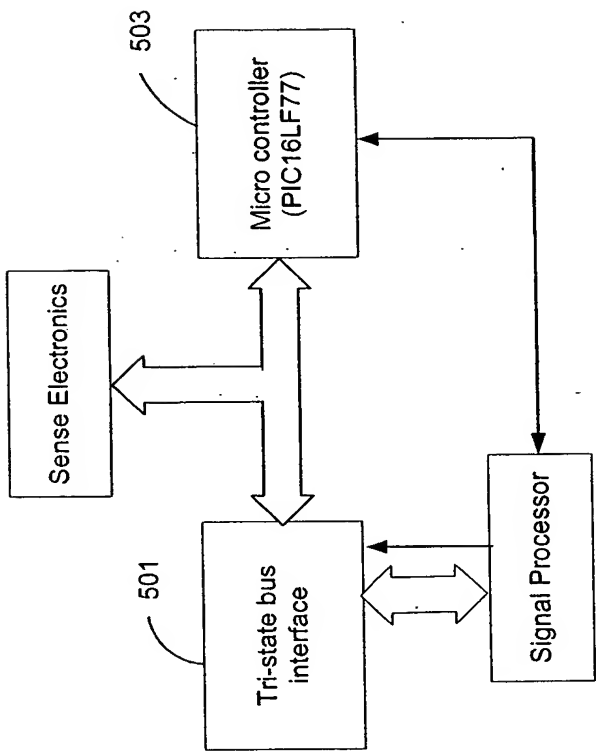


FIG. 5 (Hardware interface processor)

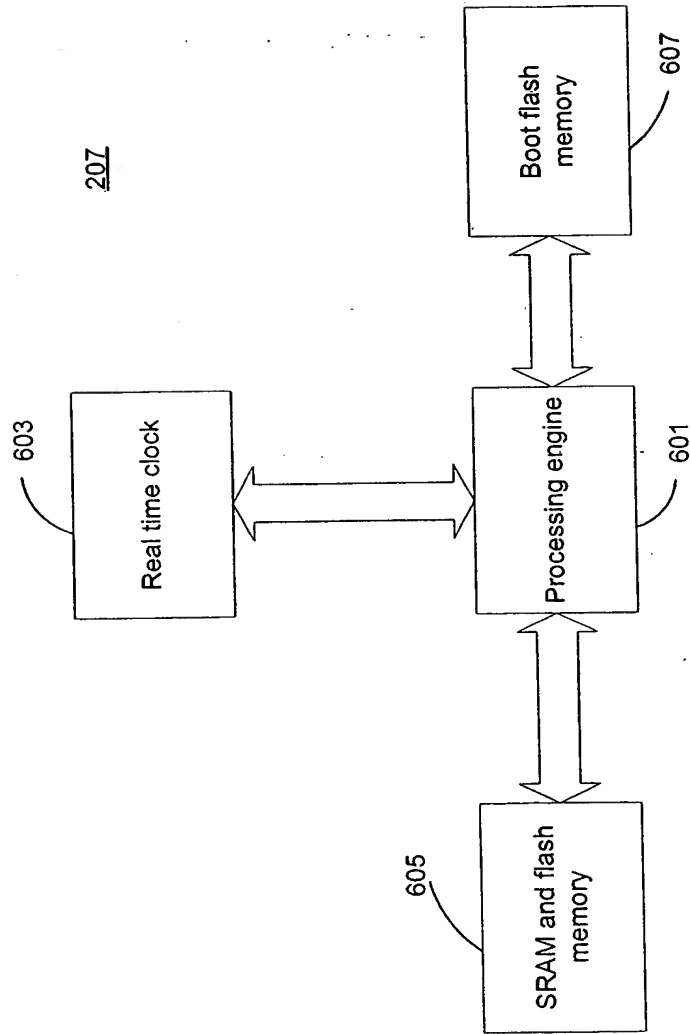


FIG. 6 (Signal processor)

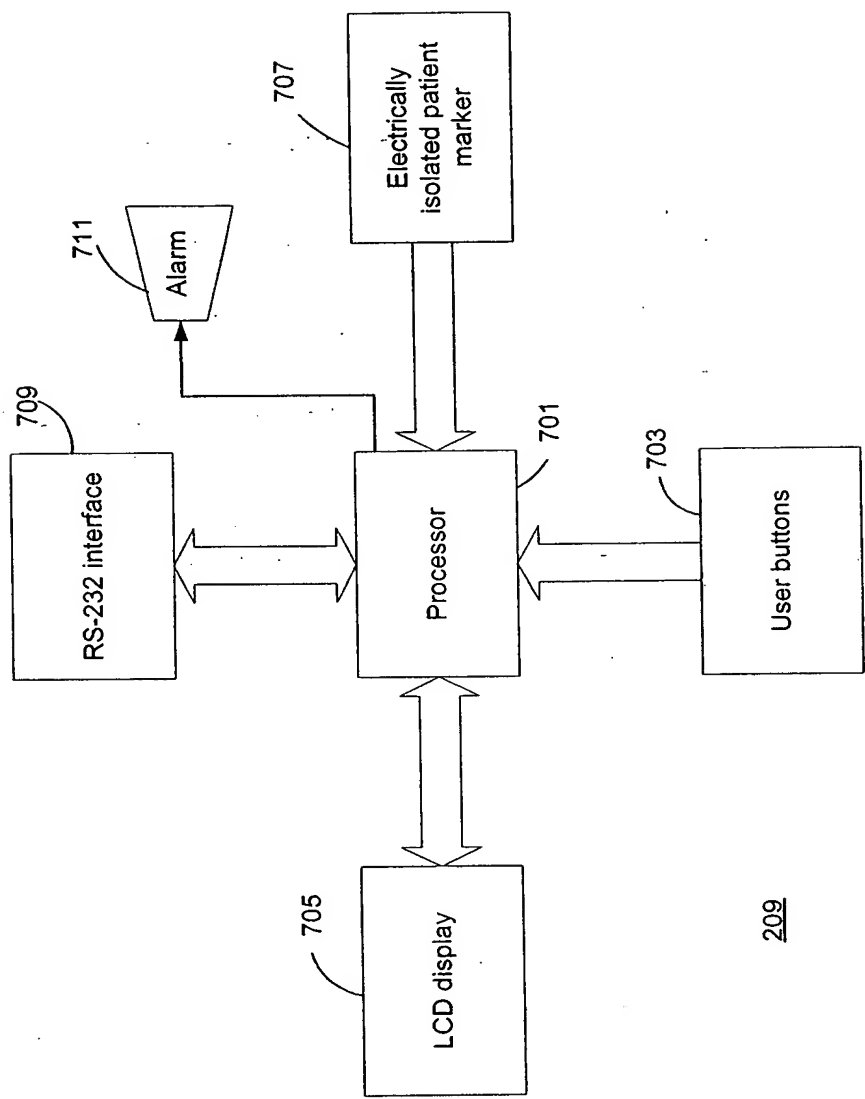


FIG. 7 (User interface processor)

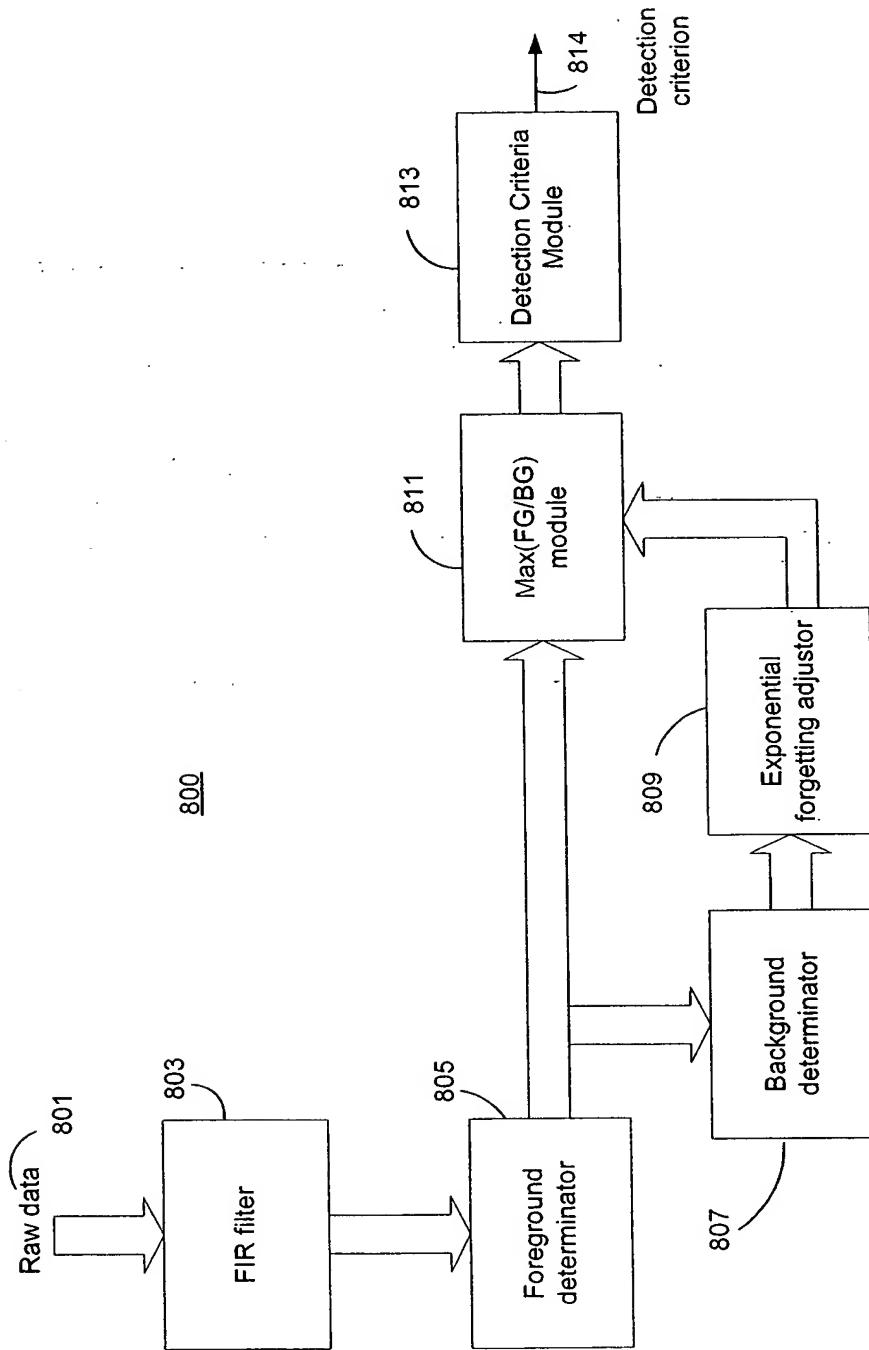


FIG. 8 (Seizure detection algorithm)

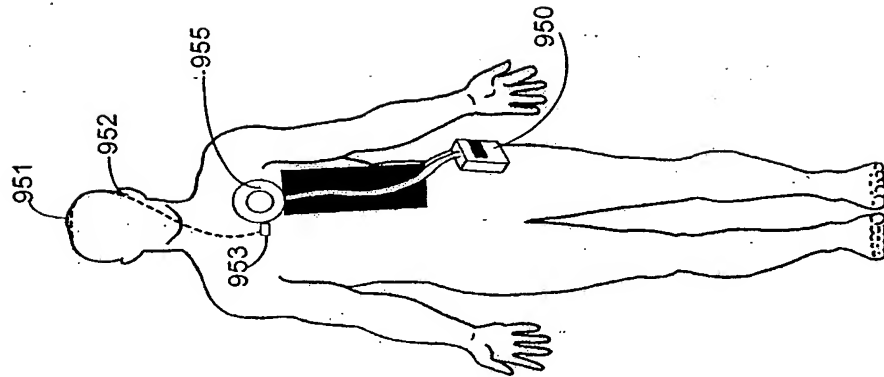


FIG. 9

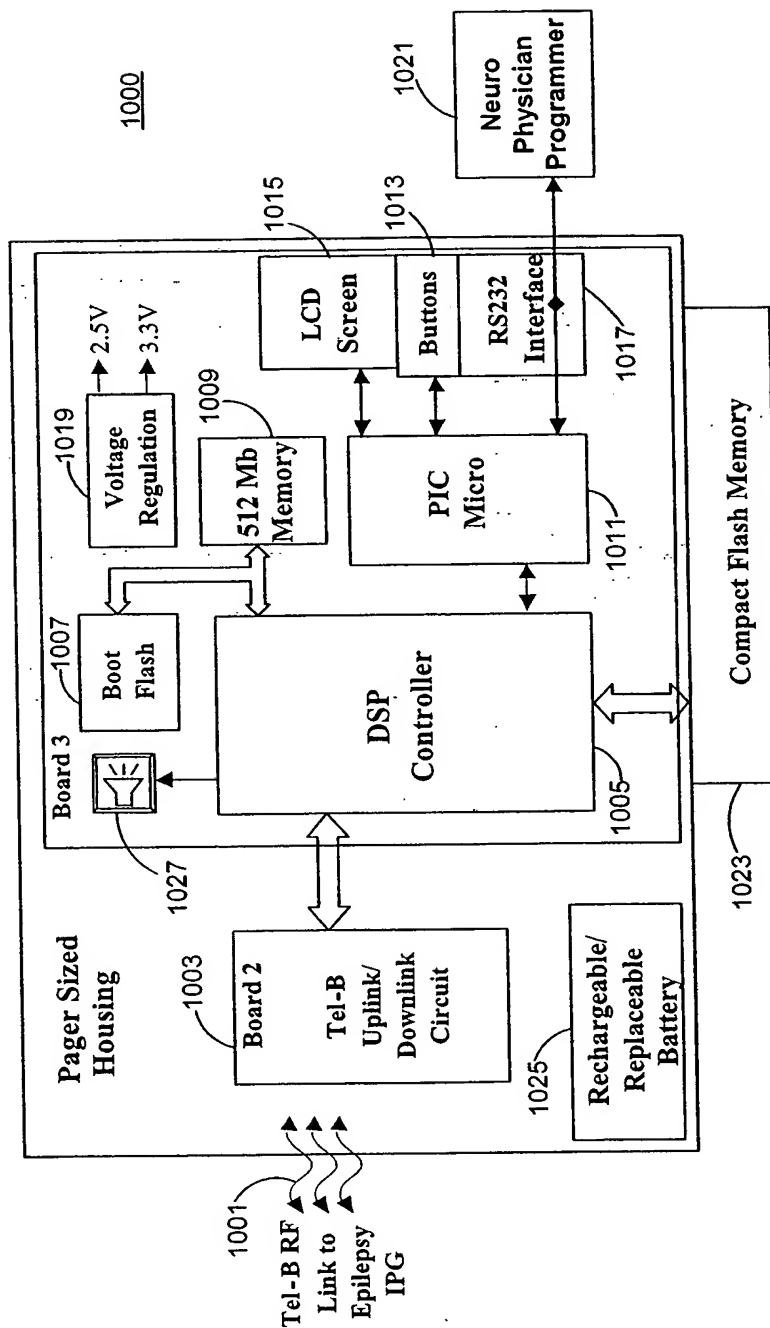


FIG. 10 (Epilepsy external device diagram)

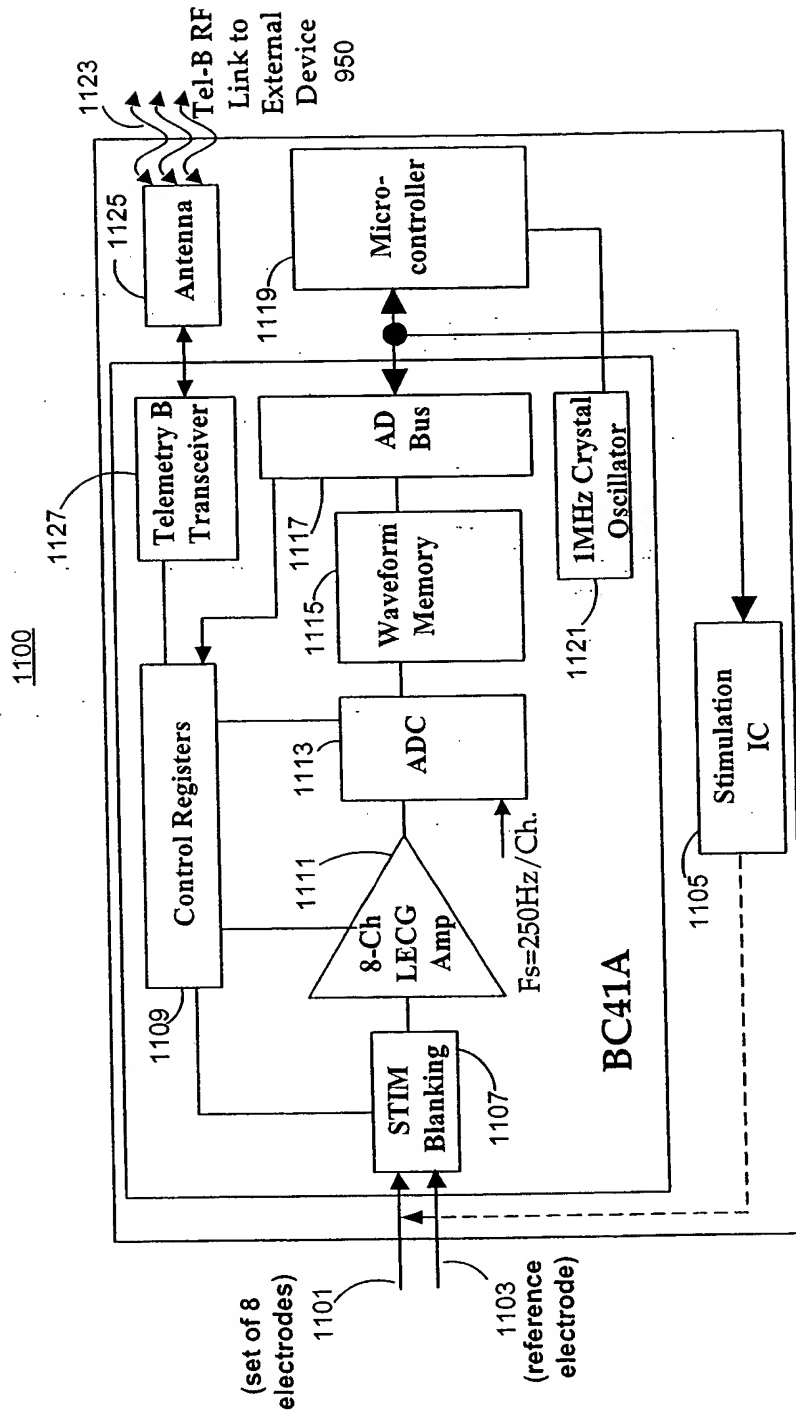


FIG. 11 (Epilepsy implantable device diagram)

FIG. 12

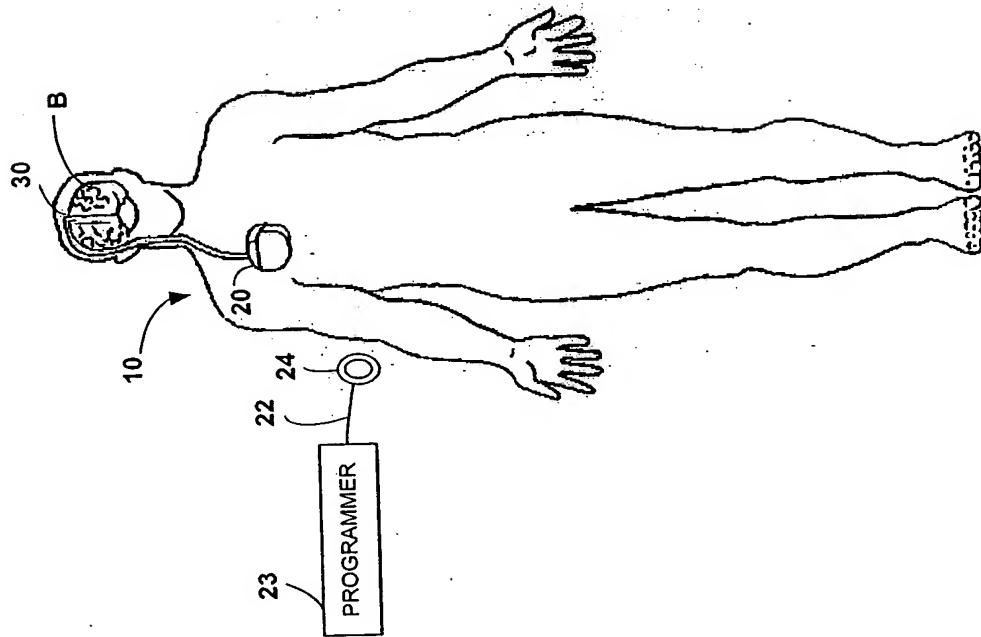
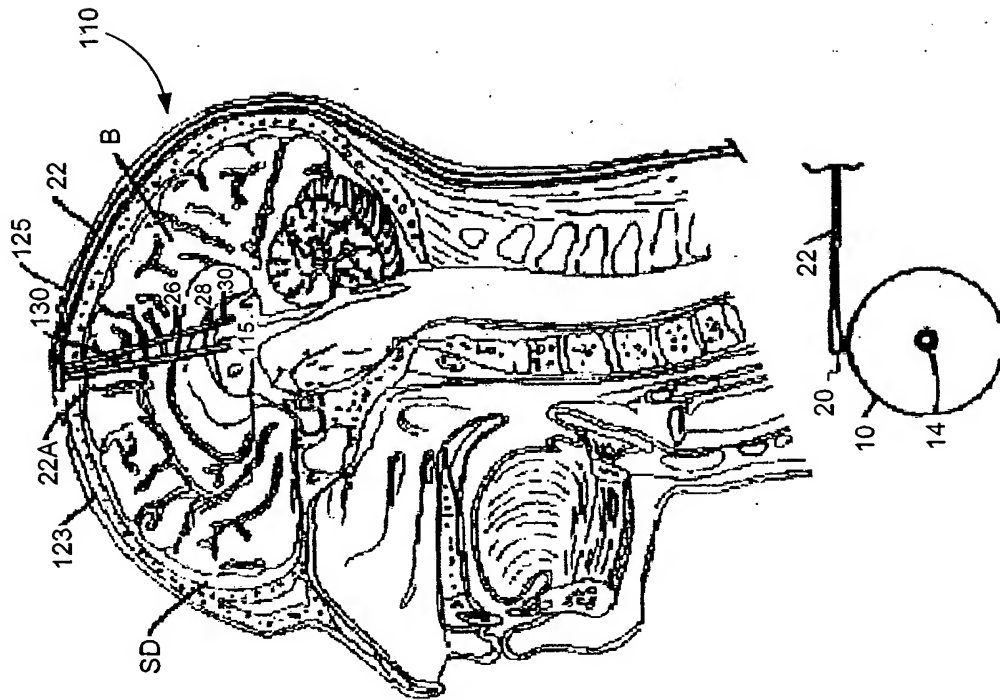


FIG. 13



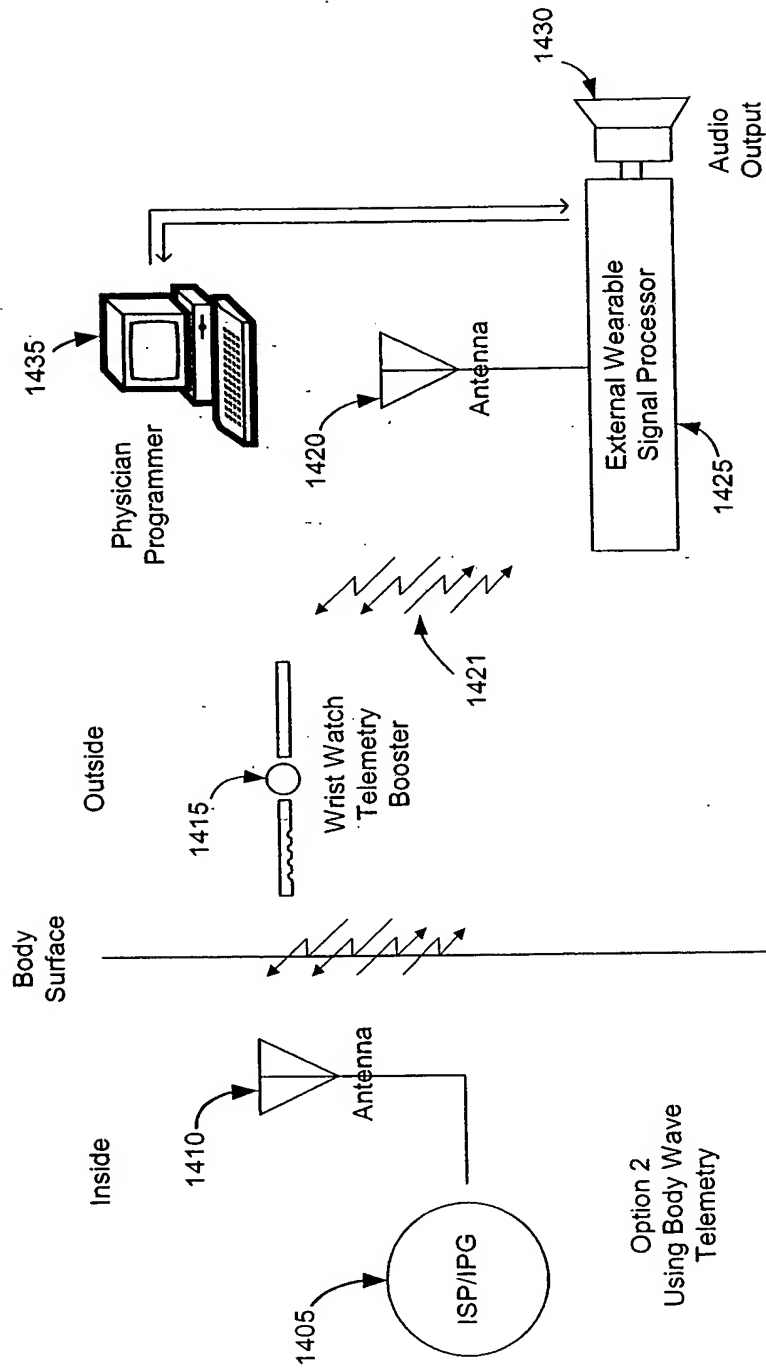


FIG. 14

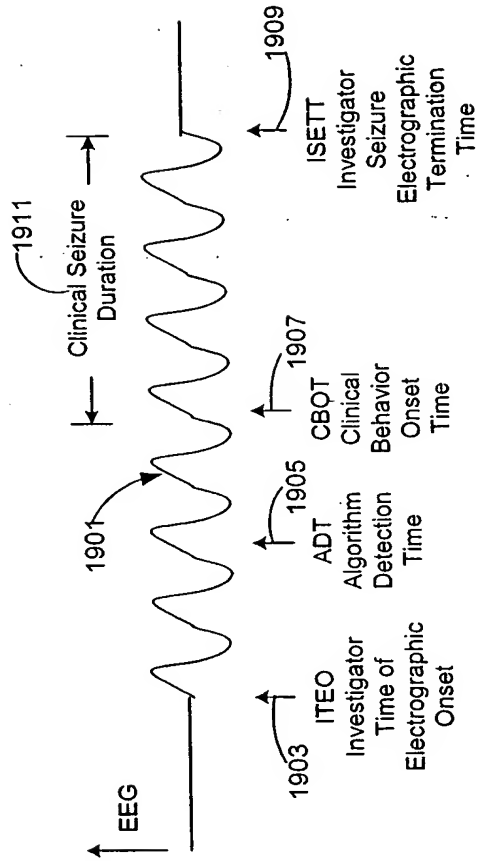


FIG. 19

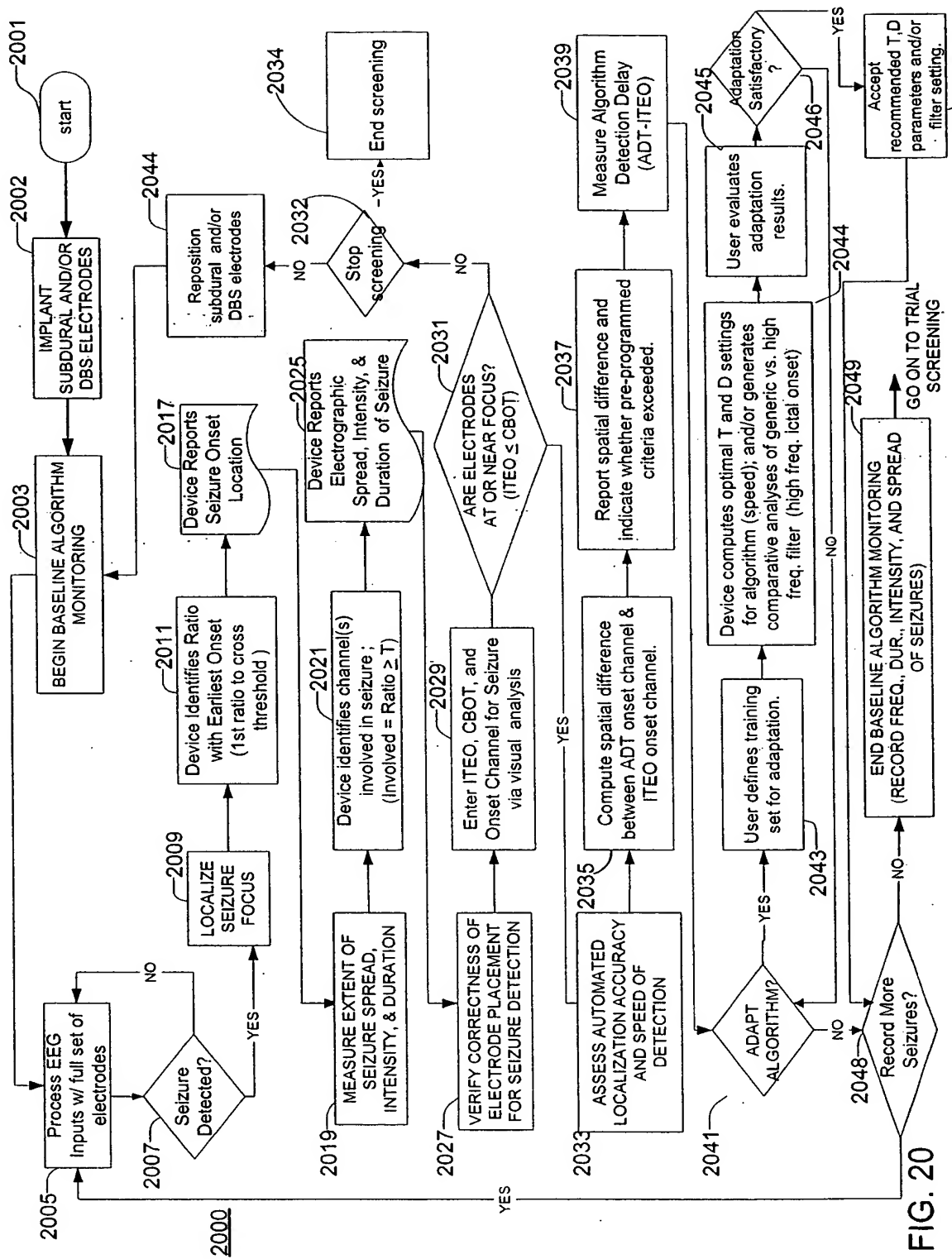


FIG. 20

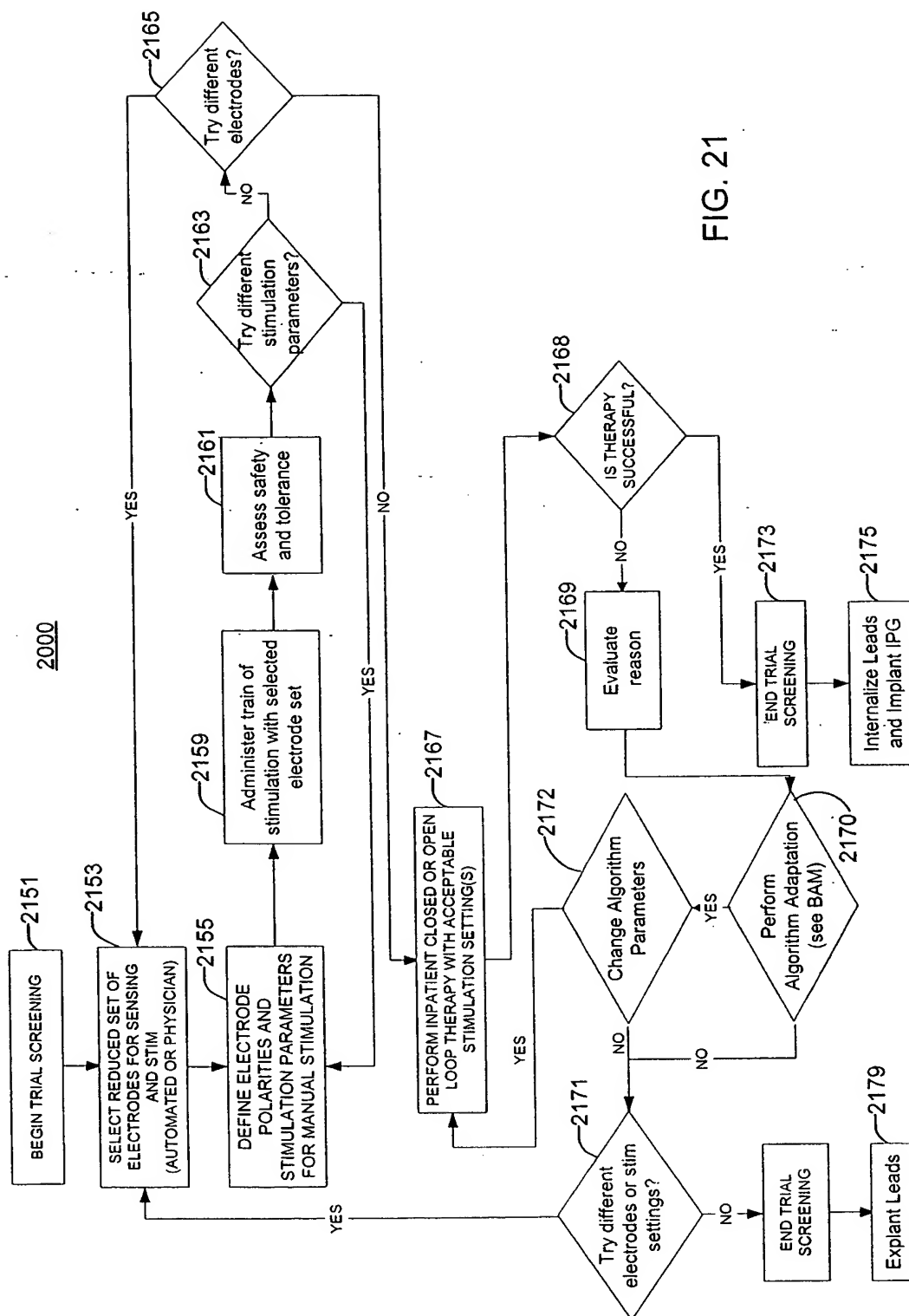


FIG. 21

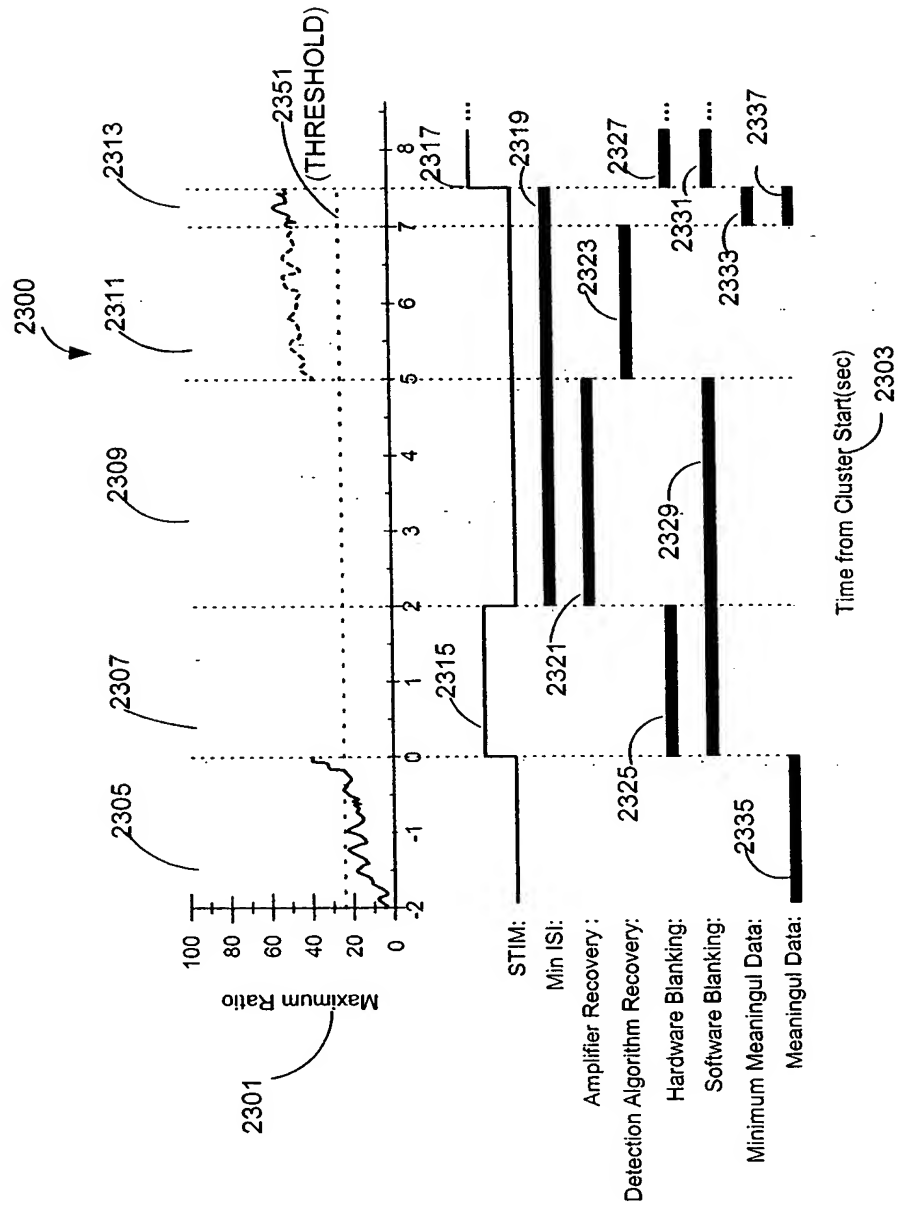
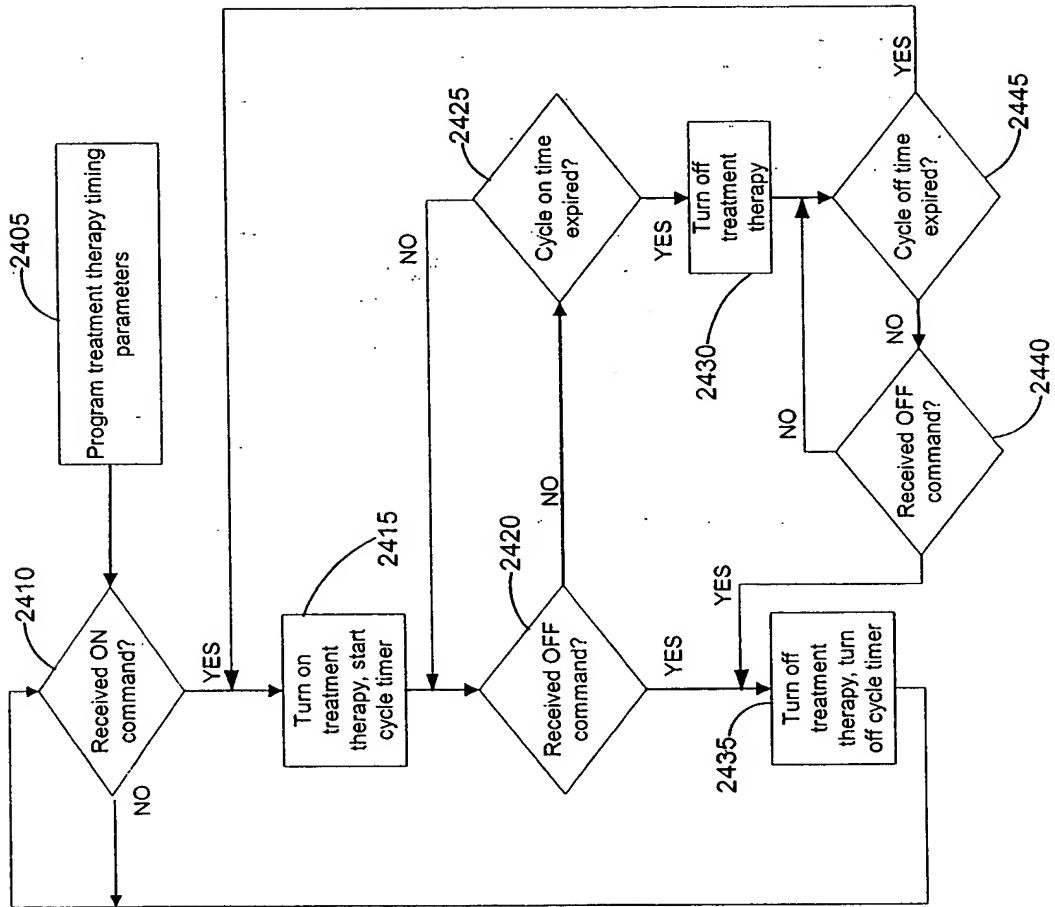


FIG. 24



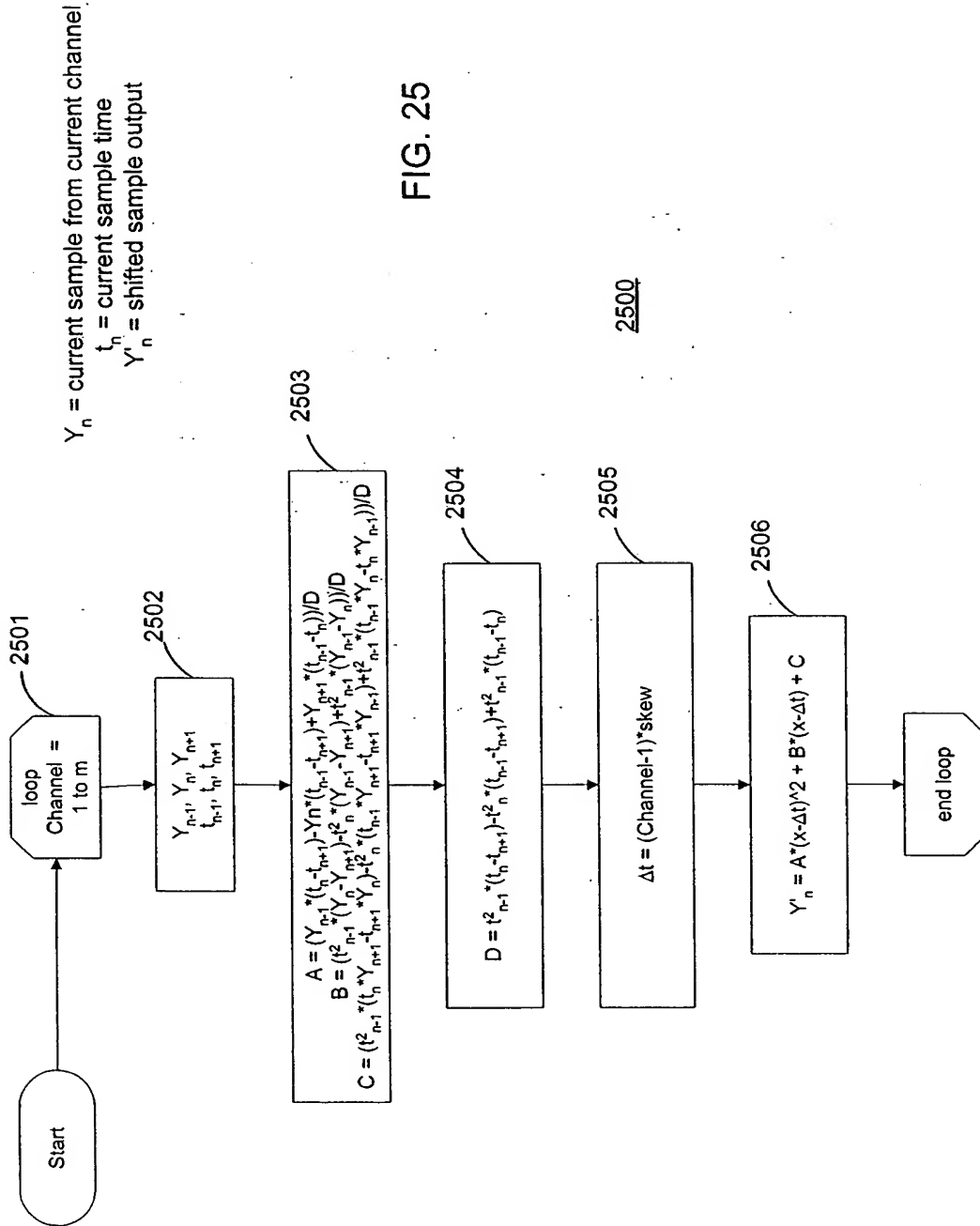
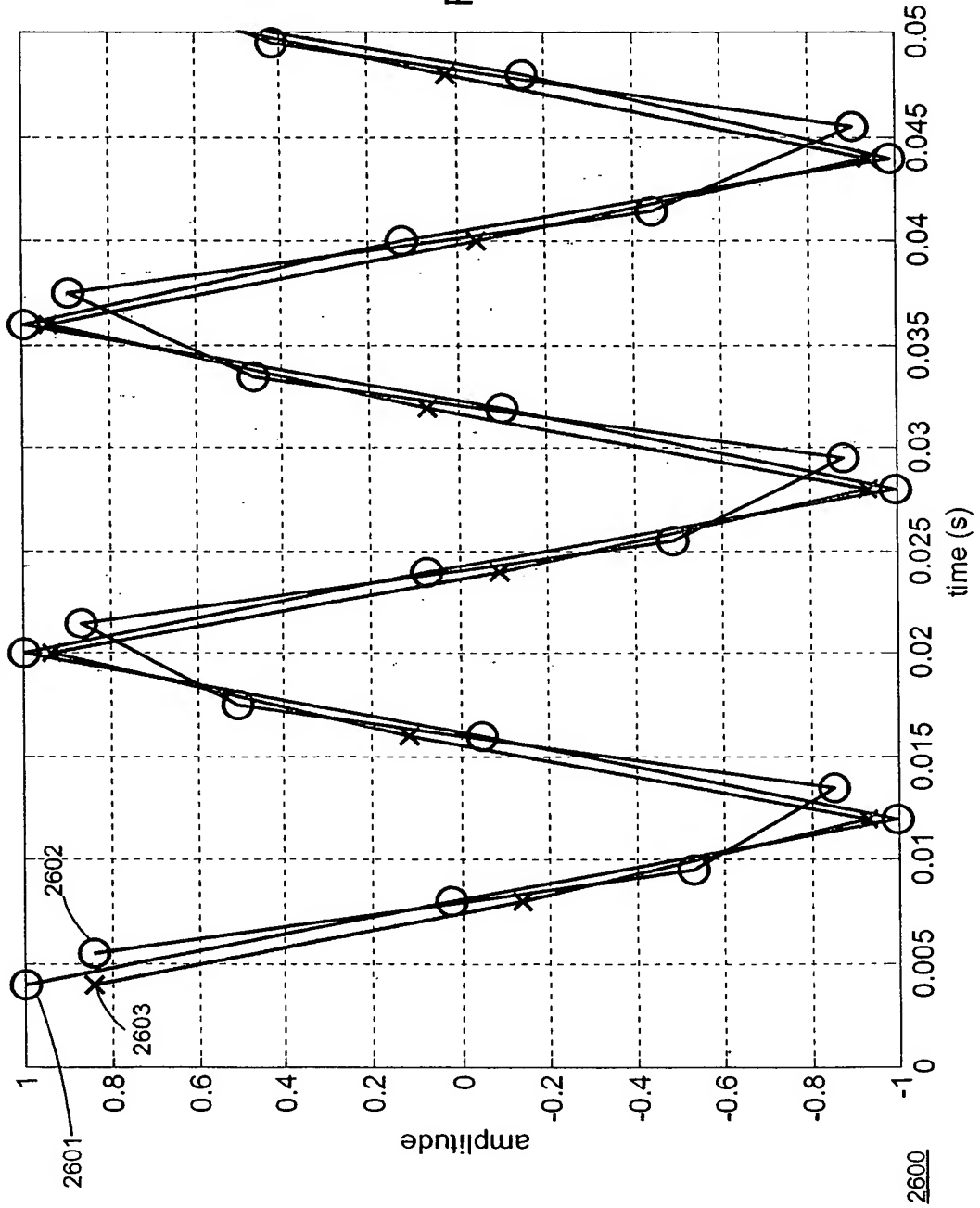


FIG. 26



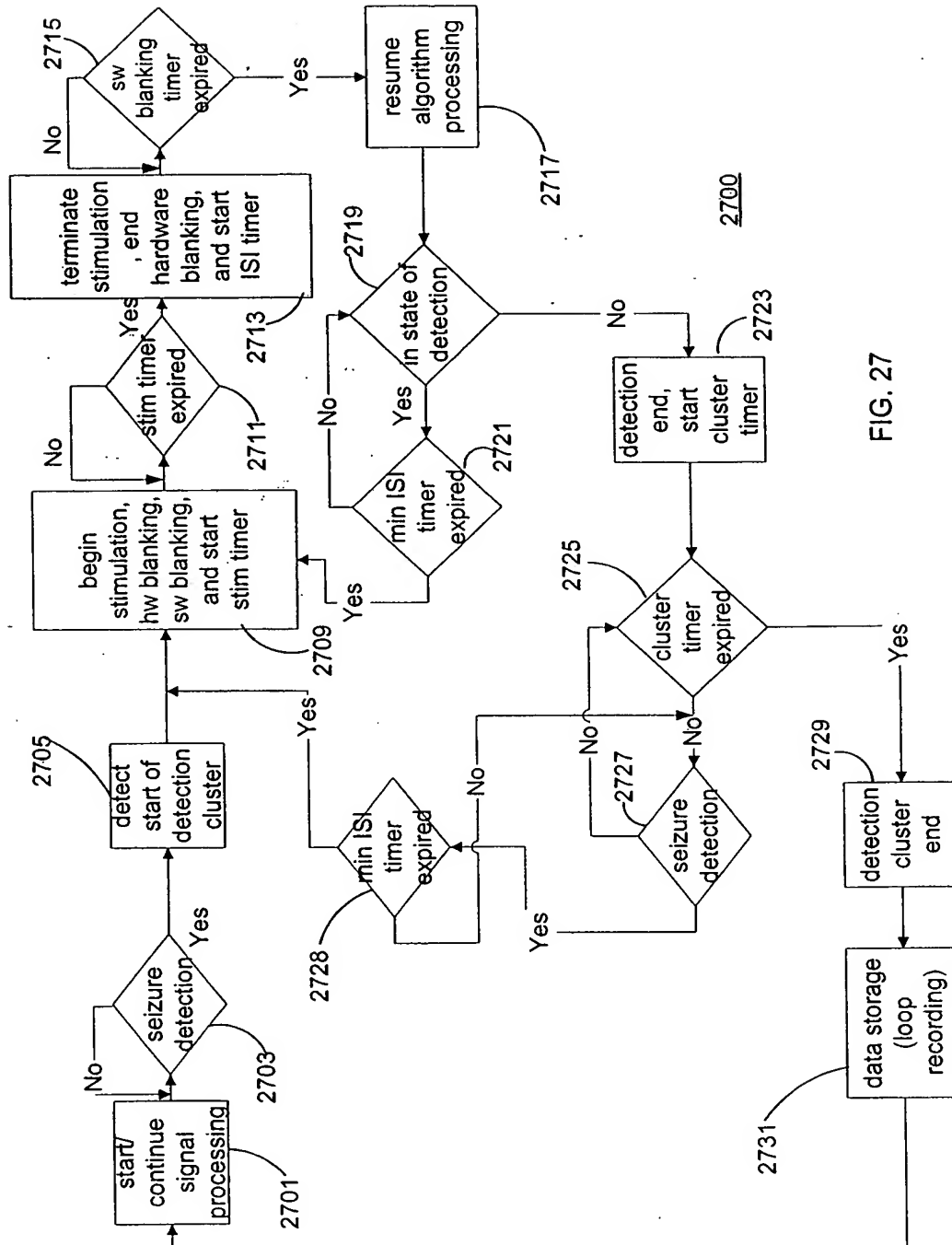


FIG. 27